

**Amendments to the Claims:**

A listing of the entire set of pending claims is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions and listings of claims in the application.

1. (previously presented) An active matrix electroluminescent display device comprising an array of display pixels, each pixel comprising:
  - an electroluminescent display element and a drive transistor for driving a current through the display element;
  - a first switch enabling power from a power supply line to be supplied to the display element;
  - a second switch for routing current from a current-measurement supply line to the display element, the first and second switches being operated in complementary manner; and
  - a control line for controlling the gate voltage applied to the drive transistor, wherein a feedback system is provided between the current-measurement supply line and the control line.
2. (previously presented) The device of claim 1, wherein the feedback system enables a gate voltage to be determined corresponding to a desired current flow through the drive transistor.
3. (previously presented) The device of claim 1 or 2, wherein the feedback system is provided in a column driver of the display device.
4. (previously presented) The device of claim 1, wherein each pixel further comprises a storage capacitor for storing a gate-source voltage of the drive transistor.
5. (previously presented) The device of claim 1, wherein each pixel further comprises an address transistor connected between the control line and the gate of the drive transistor.
6. (previously presented) The device of claim 5, wherein the address transistor, and the first and second switches are each controlled by a shared control line.

7. (previously presented) The device of claim 6, wherein the address transistor and the second switch are controlled synchronously.

8. (previously presented) The device of claim 1, wherein each switch comprises a transistor.

9. (previously presented) The device of claim 8, wherein one of the switches is an NMOS TFT and the other is a PMOS TFT.

10. (previously presented) The device of claim 1, wherein the feedback system comprises:  
a current-to-voltage converter section for providing a first voltage corresponding to the current drawn from the current-measurement supply line;  
a comparator section for comparing the first voltage with an input voltage representing the desired current; and  
a drive section for providing a voltage on the control line, the feedback loop being in equilibrium when the control line voltage provides drive of the drive transistor giving rise to a current corresponding to the input voltage.

11. (previously presented) The device of claim 1, wherein the device is operable in two modes:  
a first mode in which a desired pixel drive current is drawn from the current-measurement supply line and the feedback system generates the corresponding gate voltage for the drive transistor, the corresponding gate-source voltage for the drive transistor being stored; and  
a second mode in which a current is driven through the drive transistor and the EL display element using the stored gate-source voltage.

12. (previously presented) A method of addressing an active matrix electroluminescent display device comprising an array of display pixels, in which each pixel comprises an electroluminescent display element and a drive transistor for driving a current through the display element, the method comprising, for each pixel:  
applying a voltage to the drive transistor to drive a current through the display element, the current being drawn from a current-measurement supply line;

processing the current using feedback control circuitry outside the array of pixels and having an input representing the desired current;

generating a control voltage in the feedback control circuitry for the drive transistor using the processed current, thereby implementing a feedback control loop which reaches equilibrium when the current corresponds to the desired current, and supplying the control voltage to the pixel;

within the pixel, storing a voltage derived from the control voltage; and

applying the stored voltage to the gate of the drive transistor and drawing current from a power supply line to illuminate the display element.

13. (previously presented) The method of claim 12, wherein processing the current comprises converting the current into a voltage, and comparing the voltage with an input voltage representing the desired current to produce an amplified differential output.

14. (previously presented) The method of claim 13, wherein the control voltage comprises the amplified differential output.

15. (previously presented) The method of any one of claims 12 to 14, wherein current is drawn from the power supply line through a first switch and current is drawn from the current-measurement supply line through a second switch, the first and second switches being operated in complimentary manner, the first switch being used after an initial pixel programming phase and the second switch being used during the initial pixel programming phase.

16-20. (canceled)